

## INTERFERENCE DIGEST

Interference No. 102,725 Paper No. 14  
Name, Joseph T. Evans, Jr. et al.  
Serial No. 07/582,672 Patent No. \_\_\_\_\_  
Title, Non-Volatile Memory Circuit Using Ferroelectric Capacitor Storage Element  
Filed, 09/14/90  
Interference with S. Sheffield Eaton, Jr.

### DECISION ON MOTIONS

Examiner-in-Chief, \_\_\_\_\_ Dated, \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_

### FINAL DECISION

Board of Patent Appeals and Interferences, favorable Dated, 5/6/97  
\_\_\_\_\_  
Court, \_\_\_\_\_ Dated, \_\_\_\_\_

### REMARKS

\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_